



Drafts

BRS: \$

BRS: a

Pending

Active

L1: (7) "6369855".pn.

L2: (2) "5959659".pn.

Failed

((COURT OR EVIDENCE OR ORIGINAL) AND ((print or imprint or printing or stampin

386/46.CCLS. AND DIGITAL AND (SWITCH OR SWITCHING

"5396374.pn.

((DIFFERENT OR ENHANCED) WITH ERROR WITH (DETECTION OR CORRECTIO

Saved

S1: (2117) GRAPHICS ADJ ACCELERATOR

S2: (56) MPEG WITH (GRAPHICS ADJ ACCELERATOR)

S3: (16) TRICK AND (REVERSE WITH FRAMES WITH MEMORY)

S4: (89) TRICK AND (REVERSE WITH MEMORY)

S5: (38) TRICK AND (REVERSE WITH MEMORY) AND "386"\$.CCLS.

S6: (19) NGUYEN XA. AND TRAN XP. AND 386/68.CCLS.

S7: (111) NGUYEN XA. AND TRAN XP. AND "386"\$.CCLS.

S8: (5) NGUYEN XA. AND TRAN XP. AND "386"\$.CCLS. AND TRICK

S9: (79) NGUYEN XA. AND TRAN XP. AND "386"\$.CCLS. AND DIGITAL

S10: (2) NGUYEN XA. AND TRAN XP. AND "386"\$.CCLS. AND MUTO.IN

S11: (15) "6369855"

S12: (7) "6369855".PN.

S13: (10) TRANSPORT AND TIVO.AS.

S14: (8232) TRANSPORT AND MPEG

S15: (141) S14 AND 386/68.CCLS.

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BRS form ISR form Image Text HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Class	Inventor
1	<input type="checkbox"/>	<input type="checkbox"/>	US 5959659 A	19990928	17	MPEG-2 transport stream decoder having decoupled hardware architecture	725/152	375/240.25; 725/132		Dokic, Miroslav V.
2	<input type="checkbox"/>	<input type="checkbox"/>	US 5959659 A	19990928	17	Decoupled hardware architecture of MPEG-2 decoder				DOKIC, M V

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